International Conference on Smart Systems and Technologies 2017 (SST 2017) SST 2017 October 18-20, 2017 Faculty of Electrical Engineering, Computer Science and Information Technology Josip Juraj Strossmayer University of Osijek Osijek, Croatia Organized by the Faculty of Electrical Engineering, Computer Science and Information Technology, J.J. Strossmayer University of Osijek Technically co-sponsored by: IEEE Region 8 (pending), IEEE Croatia Section, Communications Chapter, Computer Chapter, Systems Man and Cybernetics Chapter, Reliability Chapter, Ministry of Science and Education of the Republic of Croatia, CIGRÉ - Croatian National Committee, Croatian Academy of Engineering,

CALL FOR PAPERS

General Chair

Drago Žagar

J.J. Strossmayer University of Osijek, Croatia

Program Co-Chairs

Goran Martinović

J.J. Strossmayer University of Osijek, Croatia

Snježana Rimac Drlje

J.J. Strossmayer University of Osijek, Croatia

Registration Chair

Dražen Slišković

J.J. Strossmayer University of Osijek, Croatia

Publication Chair

Kruno Miličević

J.J. Strossmayer University of Osijek, Croatia

Publicity/Web Chair

Josip Balen

J.J. Strossmayer University of Osijek, Croatia

Steering Committee

Leo Budin

Croatian Academy of Sciences and Arts, Croatia University of Zagreb, Croatia

Matjaž Colnarič

University of Maribor, Slovenia

Bojan Čukić

University of North Carolina at Charlotte, USA

Sven Gotovac

University of Split, Croatia

Mislav Grgić

University of Zagreb, Croatia

Željko Hocenski

J.J. Strossmayer University of Osijek, Croatia

Ignac Lovrek

University of Zagreb, Croatia

Gabriel-Miro Muntean

Dublin City University, Ireland

Srete Nikolovski

J.J. Strossmayer University of Osijek, Croatia

Igor Papič

University of Ljubljana, Slovenia

Davor Pavuna

École Polytechnique Fédérale de Lausanne (EPFL), Switzerland

Nedjeljko Perić

University of Zagreb, Croatia

Jože Pihler

University of Maribor, Slovenia

Josip Stjepandic

PROSTEP AG, Darmstadt, Germany

Andrea M. Tonello

Alpen-Adria-Universität Klagenfurt, Austria

Borut Žalik

University of Maribor, Slovenia

Smart Systems and Technologies (SST) is a conference organized and hosted by the Faculty of Electrical Engineering, Computer Science and Information Technology, Josip Juraj Strossmayer University of Osijek. The conference will provide a platform for researchers and practitioners interested in the theory and practice of smart systems and technologies related to electrical engineering, communications, computer science and engineering, control systems, robotics, as well as interdisciplinary research and applications.

Topics for submission include, but are not limited to, the following:

Affective Computing

Agriculture Automation

Applied Electromagnetics in Smart Systems

Big Data and Smart Systems

Biometrics and Smart Systems

Cloud Computing

Communication Systems

Computational Intelligence Control Systems and Robotics

Cyber-Physical Systems

Dependable Computer Systems

Embedded Computer Systems

Green Communication

Green Computing

High Performance Computing Image and Video Processing

Intelligent Power Systems and Environments

Intelligent Transportation Systems

Internet of Things

Mobile and Wireless Networks

Security and Privacy
Smart Antennas

Smart Health

Smart Homes and Cities Smart Systems and Technologies in Education

Smart Systems for Green Energy Smart Systems in Business

Software Engineering for Smart Systems

PAPER SUBMISSION AND PUBLICATION

All papers submitted to the conference must not have been published, submitted or accepted for publication elsewhere. IEEE policy regarding plagiarism and duplicate submission/publication will be strictly enforced. All papers will be judged on the basis of their clarity, relevance, originality, and contribution. Manuscripts will be limited to 6 (IEEE style) pages, up to 8 pages with additional costs. Papers should be submitted electronically via the on-line submission system (Easy-Chair). Paper submission should follow the IEEE double-column format for conferences (templates will be available via the conference web site). Submitted papers will undergo a peer review process coordinated by the Program Committee. All submissions must be in English. Authors of accepted papers are required to register for the conference and attend the conference in order to have the paper included in the SST 2016 Proceedings and submitted for posting to the IEEE Xplore database and other indexing databases. Selected papers presented at the Conference will be invited for possible journal publication (Technical Gazette, International Journal of Electrical and Computer Engineering Systems) in expanded form and after a new round of review

IMPORTANT DATES

30 April, 2017 Conference paper submission (extended deadline)

15 June, 2017 Notification of conference paper acceptance

15 July, 2017 Final conference paper submission

19 August, 2017 Conference registration

SPECIAL SESSION AND WORKSHOP PROPOSALS

A special session proposal (up to 2 pages) should contain the session organizer(s) short bio and contact information, title, summary, and objectives. All papers submitted to a special session must comply with paper submission and publication guidelines and will be reviewed according to the review procedure.

Workshop proposals are also solicited. A workshop should focus on a specific theme related to the main conference topics. The proposal (up to 2 pages) should describe the theme and scope of the workshop and contain workshop chair(s) bio and contact information and a tentative list of workshop program committee members. Special session and workshop proposals may be submitted by e-mail to the Program Co-Chairs.

IMPORTANT DATES

1 March, 2017 Workshop and special session proposal submission
15 March, 2017 Workshop and special session proposal acceptance
30 April, 2017 Workshop/special session paper submission

15 June, 2017 Notification of workshop/special session paper acceptance

15 July, 2017 Final paper submission

25 August, 2017 Workshop/special session registration

CONTACT

Web: sst-conference.org | E-mail: sst2017@etfos.hr